



Fig. 2A PRIOR ART

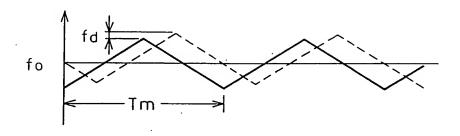


Fig. 2B

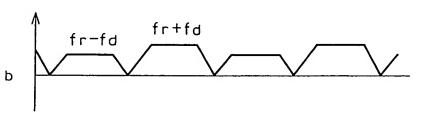
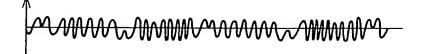


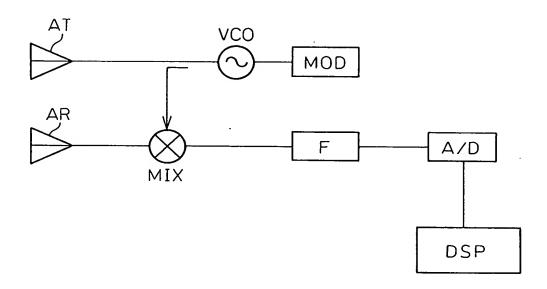
Fig. 2C PRIOR ART



Application No. 10/757,559 Inventor(s): Osamu Isaji Title: RADAR APPARATUS

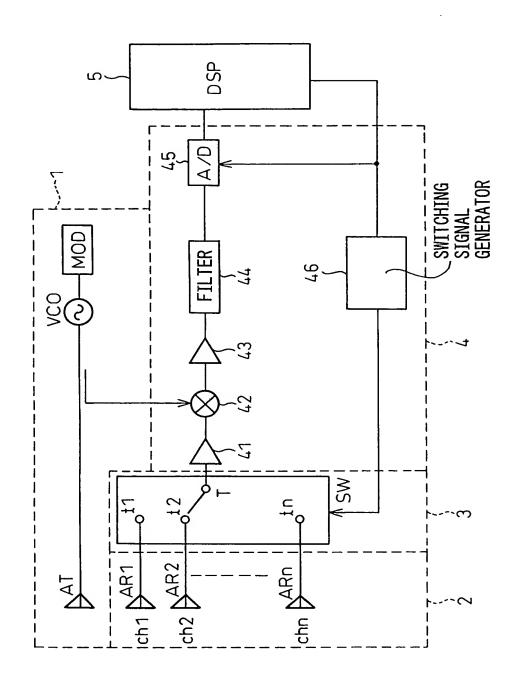
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Fig. 3
PRIOR ART



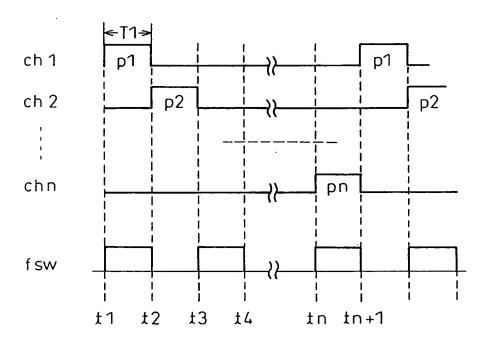
DSP PH-SFT PH-SF PH-SF A/Dn A/D2 MOD Fig. 4
PRIOR ART F2F √MIX1 MIX2 AMPn AMP1 AMP2 ARU AR1

Fig. 5
PRIOR ART



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Fig.6



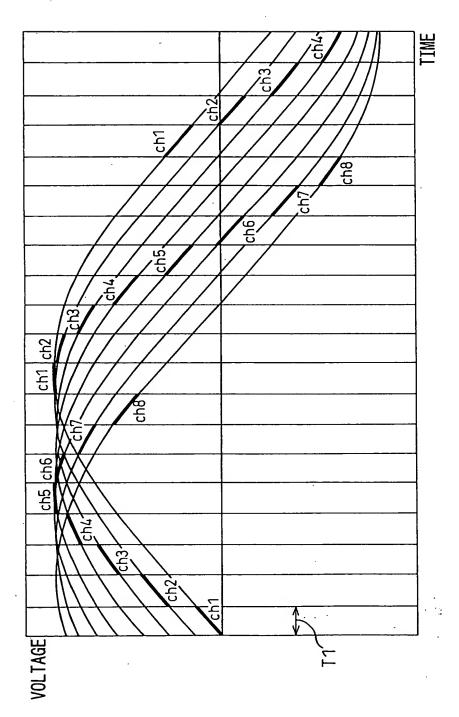


Fig.7

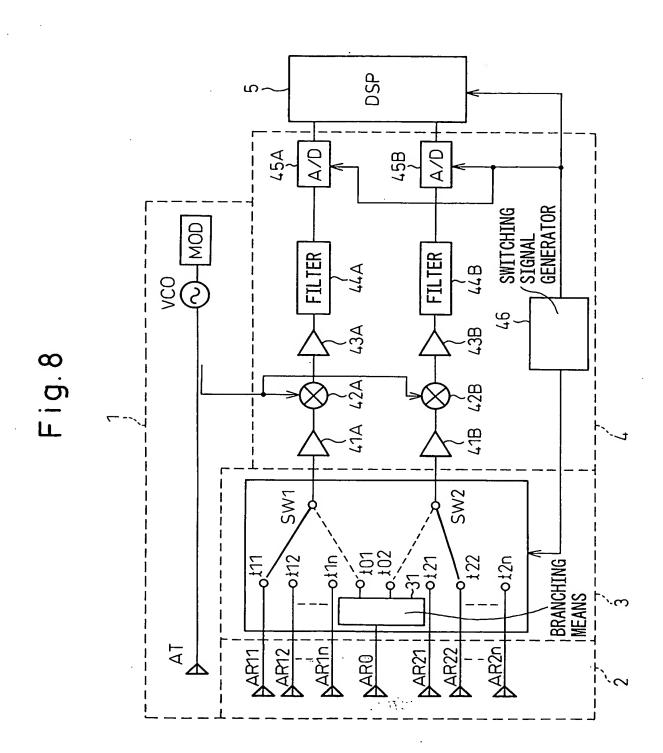


Fig.9

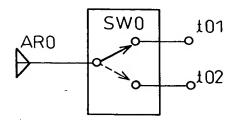
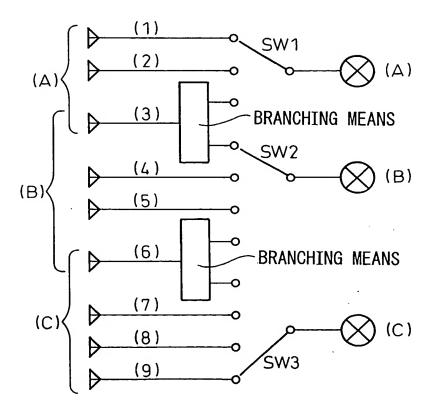


Fig.10



Application No. 10/757,559 Inventor(s): Osamu Isaji Title: RADAR APPARATUS

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Fig.11B

Fig.11A

<u>(</u>6) (9)(m (2)(5) 8 (7) (6) (3) (9) (2) (2) $[\infty]$ (7) RECEIVER SECTION (A) RECEIVER (C) SECTION (C) RECEIVER (SECTION (B)

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RECEIVER SECTION (B)

3)

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RECEIVER SECTION (A)

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(9)

RECEIVER SECTION (C)

SWITCHING FOR THE CASE WHERE CORRECTIONS ARE NOT APPLIED

SWITCHING FOR THE CASE WHERE CORRECTIONS ARE APPLIED